

## **BIBLIOGRAFÍA.**

Miles Murdocca; Vincent Heuring, “*COMPUTER ARCHITECTURE AND ORGANIZATION, An Integrated Approach*”, Ed Wiley, United States of America 2007.

John Paul Shen; Mikko H. Lipasti, “*MODERN PROCESSOR DESIGN, Fundamentals of Superscalar Processors*”, Ed Mc Graw Hill, 2005.

Carl Hamacher; Zvonko Vranesic; Safwat Zaky, “*COMPUTER ORGANIZATION*”, International Edition, Fifth Edition, 2002.

David L. Weaver, “*OPENSPARC INTERNALS BOOK*”, Sun Microsystems, Inc. First Edition, 2002.

Savage Jesús, Vázquez Gabriel, “*DISEÑO DE MICROPROCESADORES*”, Facultad de Ingeniería, México, 2004.

Pardo Fernando, “*VHDL, LENGUAJE PARA DESCRIPCIÓN Y MODELADO DE CIRCUITOS*”, Universidad de Valencia, 1997.

“*Arquitectura de una Computadora*”, Universidad Tecnológica Nacional, facultad Regional Rosario, Departamento Ingeniería Eléctrica.

ALTERA CORPORATION, Flex 10K Embedded Programmable Logic Device Family Data Sheet.

ALTERA CORPORATION, Max 7000 Programmable Logic Device Family Data Sheet.

XILINX, Spartan-3 FPGA Family Complete Data Sheet, 2006.

OpenSPARC™ T1 Microarchitecture Specification, Sun Microsystems, August 2006.

[www.sun.com](http://www.sun.com)

<http://www.infor.uva.es/~fernando/asignaturas/microp/pentium4.pdf>

[http://microprocesadoresymicrocontroladores.blogspot.com/2008\\_04\\_01\\_archive.html](http://microprocesadoresymicrocontroladores.blogspot.com/2008_04_01_archive.html)

Capítulo 2

SOBRE SPARC

<http://www.cpu-collection.de/?l0=i&i=1999&s=big&tb=1&n=0&sd=1> (own website)

<http://www.oracle.com/us/sun/index.html>

<http://www.dcc.uchile.cl/~rbaeza/cursos/proyarq/mroco/page3.htm>

<http://www.alegsa.com.ar/Dic/sparc.php>

<http://es.sun.com/products/pdf/sparc-t5240.pdf>

[http://www.worldlingo.com/ma/enwiki/es/SPARC#External\\_links](http://www.worldlingo.com/ma/enwiki/es/SPARC#External_links)

<http://www.apuntesdeinformatica.netai.net/foro/contenidos/simm/Unidades.Didactic as/Unidad.Didactica.2/2.Arquitectura/Componentes.HW.de.un.PC/Fabricantes.HW .para.PC.pdf>

<http://redalyc.uaemex.mx/pdf/730/73012215014.pdf>

<http://www.azc.uam.mx/publicaciones/enlinea2/num1/1-2.htm>

[www.OpenSPARC.net](http://www.OpenSPARC.net)

## SOBRE INTEL

IA-32 Intel Architecture Software Developer's Manual Volume 1: Basic Architecture at <https://developer.intel.com/design/pentium4/manuals/245470.htm>.

IA-32 Intel Architecture Software Developer's Manual Volume 2: Basic Architecture at <https://developer.intel.com/design/pentium4/manuals/245470.htm>.

Peter N. Glaskowsky, Pentium 4 (Partially) Previewed, Micro Processor Online, Agosto, 2001

Intel Pentium 4 Processor Optimization Reference Manual at <http://developer.intel.com/design/pentium4/manuals/248966.htm>.

<http://www.tomshardware.com>

## SOBRE MOTOROLA

Hamacher, Vranesic, Zaky, "Computer Organization", International Edition, Mc Graw Hill, fifth edition 2002.

<http://cedicyt.usach.cl/microcomputadores/2004/grupo3/Jose%20Morey/MICROCOMPUTADORES/PRIMERA.htm>